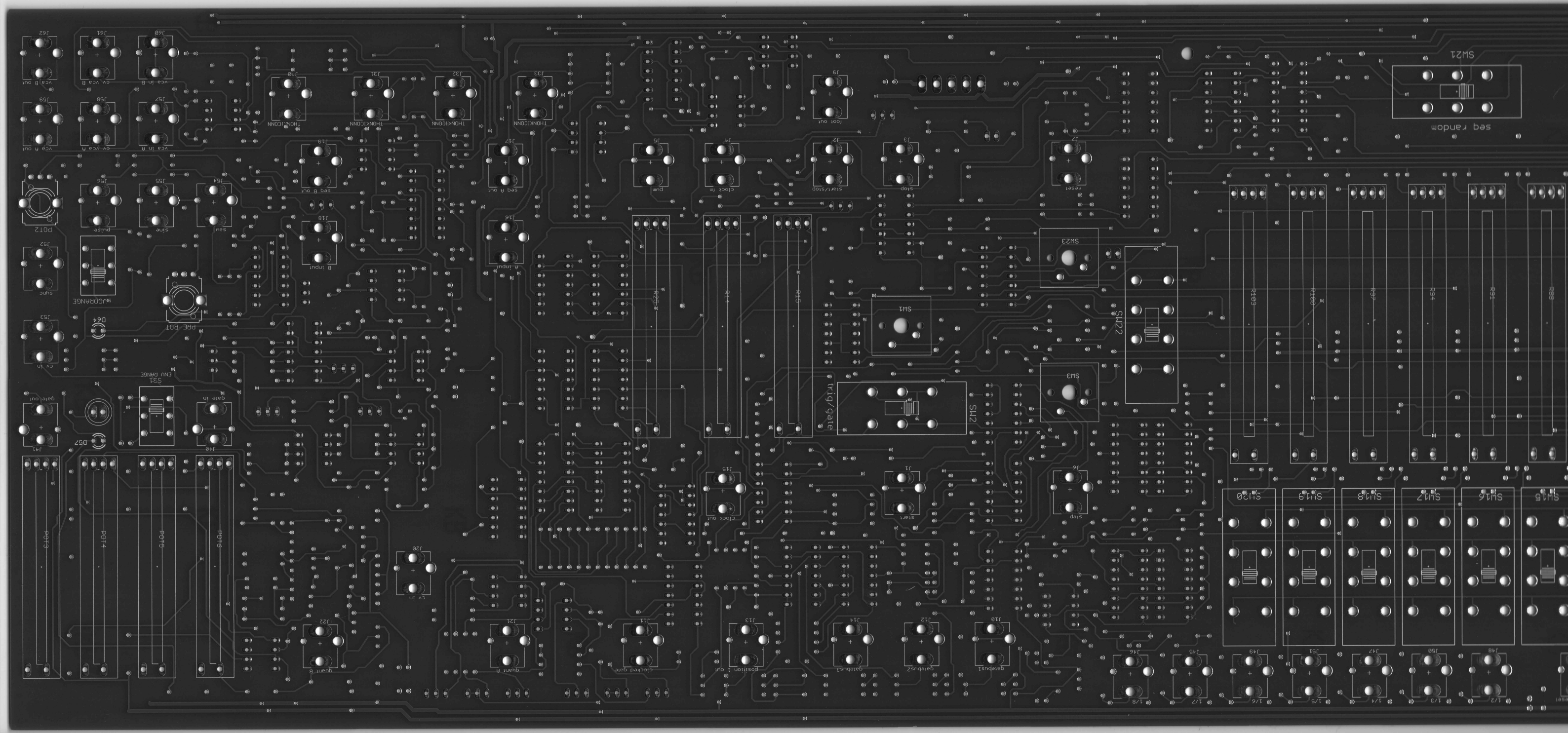
seq random
SW21
SW22
SW23
SW1
SW2
SW3
trig/gate
start/stop
reset
stop
start
step
clock in
clock out
pulse
seq r out
seq a out
A input
B input
sine
saw
pulse
sync
cv in
gate in
gate out
LFO RANGE
S31
PRE-P01
D64
D57
P012
JACKRANGE
quant A
quant B
clocked gate
position out
gatebus1
gatebus2
gatebus3
cv in
THONKICONN
vca in A
cv vca A
vca A out
vca in B
cv vca B
vca B out
loop out
R22
R14
R15
R23
R24
R25
R26
R27
R28
R29
R30
R31
R32
R33
R34
R35
R36
SW15
SW16
SW17
SW18
SW19
SW20